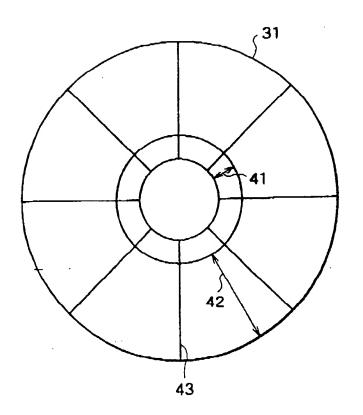


FIG.2



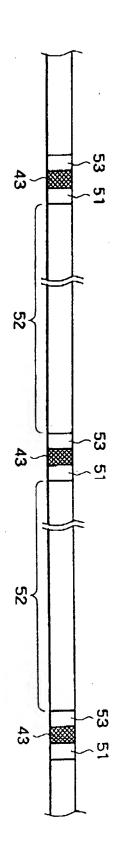
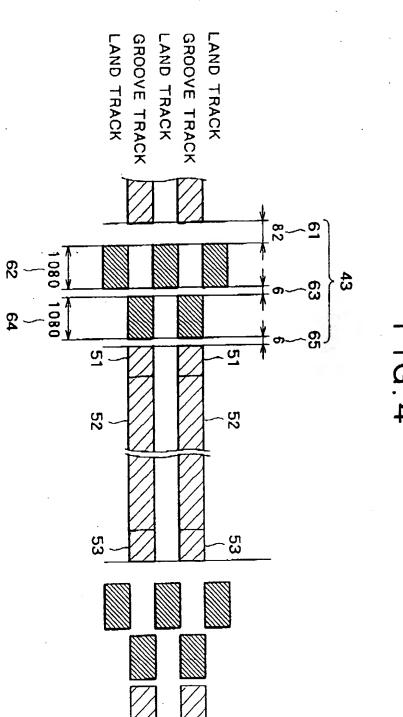
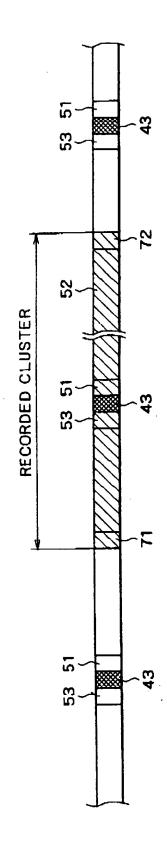


FIG.3



=1G.4



= 1G.5

1000

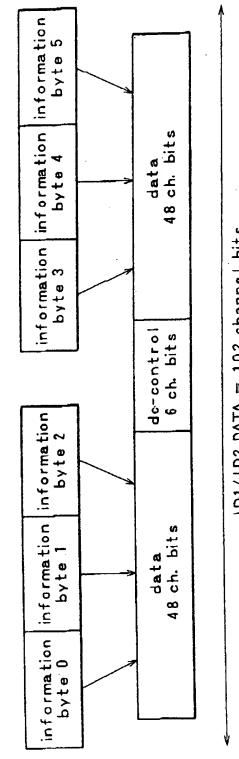
F1G.6

						1				
	VF01	PrA1	AMI	101	PoA1	VF02/DI	Pr A2	AM2	102	PoA2
7	414	30	21	102	မှ	288	30	21	102	9

LAND HEADER 62/GROOVE HEADER 64

F1G.7

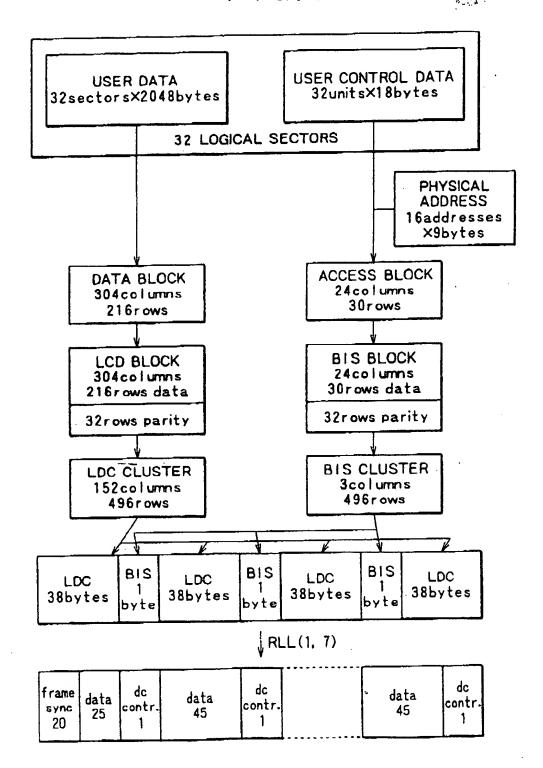
A. Serve



ID1/ID2 DATA = 102 channel bits

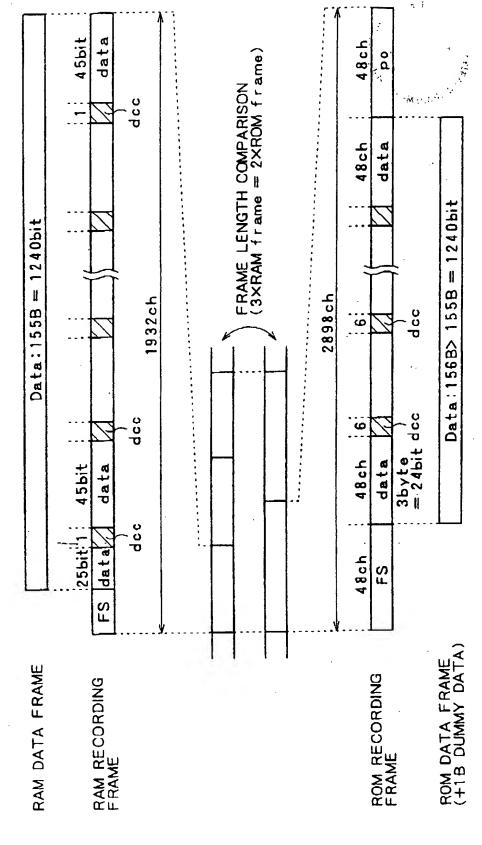
3 / -

FIG.8



F1G.9

100 T



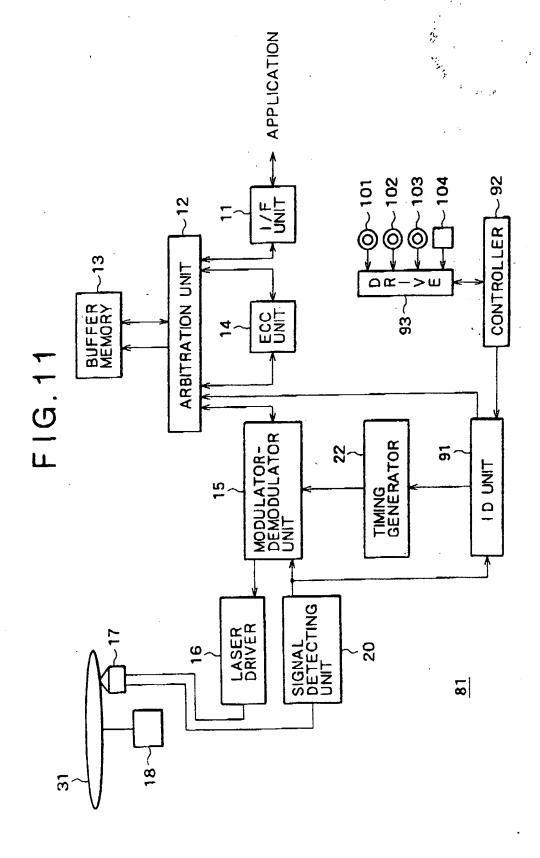
F1G.10

A 100

RAMES 1) ENT			•
DATA FI (RON /SEGM	68 68 68	1 1 1 1 1 1 .	••
DATA FRAMES (RAM) /SEGMENT	(102 (102(103)) (102(104))	105 106 107 108 110	• •
TOTAL FRAMES DATA FRAMES (ROM) (ROM) / SEGMENT / SEGMENT / SEGMENT	(105(106) (105(101))	108 109 110 111 112	• •
ZONE NUMBER	(2-) 9- (2-) 9- -0	-5 -4 -3 -2 -1	•
CONTENTS	ROM DATA ROM DATA ROM DATA	test test test test DM user data	
	ROM AREA	RAM AREA	

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1,184



Seattle Comments